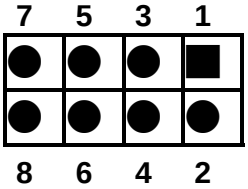
PE-6S-R31

Aug 27 2007

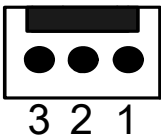
6-Slot PICMG 1.3 PCIe to PCI Bridge Backplane Ver.3.1

1 USB1、USB2: External 4 Port USB Connectors From the Mainboard



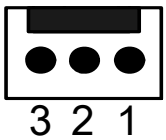
USB1、USB2			
PIN	DESCRIPTION	PIN	DESCRIPTION
1	VCC	2	GROUND
3	DATA-	4	DATA+
5	DATA+	6	DATA-
7	GROUND	8	VCC

2 FAN1、FAN2、FAN3、FAN4 : Fan Connector



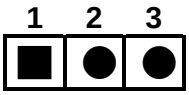
PIN	DESCRIPTION
1	Ground
2	+12V
3	NC

3 CN1 : Backplane to Mainboard ATX Connector



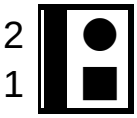
PIN	DESCRIPTION
1	GND
2	PSON#
3	5VSB

4 CN2 : PCIE X16 SDVO Control Signal Pin



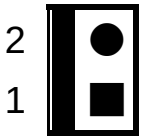
PIN	DESCRIPTION
1	EXP_EN
2	SDVO_CLK
3	SDVO_DAT

5 CN3 : External Power On Button



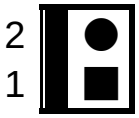
PIN	DESCRIPTION
1	GND
2	PWRBT#

6 CN4 : External Reset Button Connector



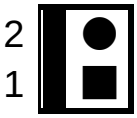
PIN	DESCRIPTION
1	GND
2	SHB_RST#

7 CN5 : External Power Good Connector



PIN	DESCRIPTION
1	GND
2	PWRGD

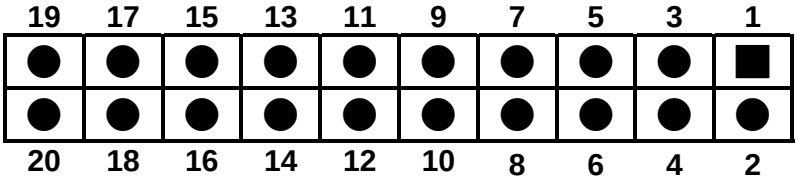
8 CN6: External System Management BUS Connector

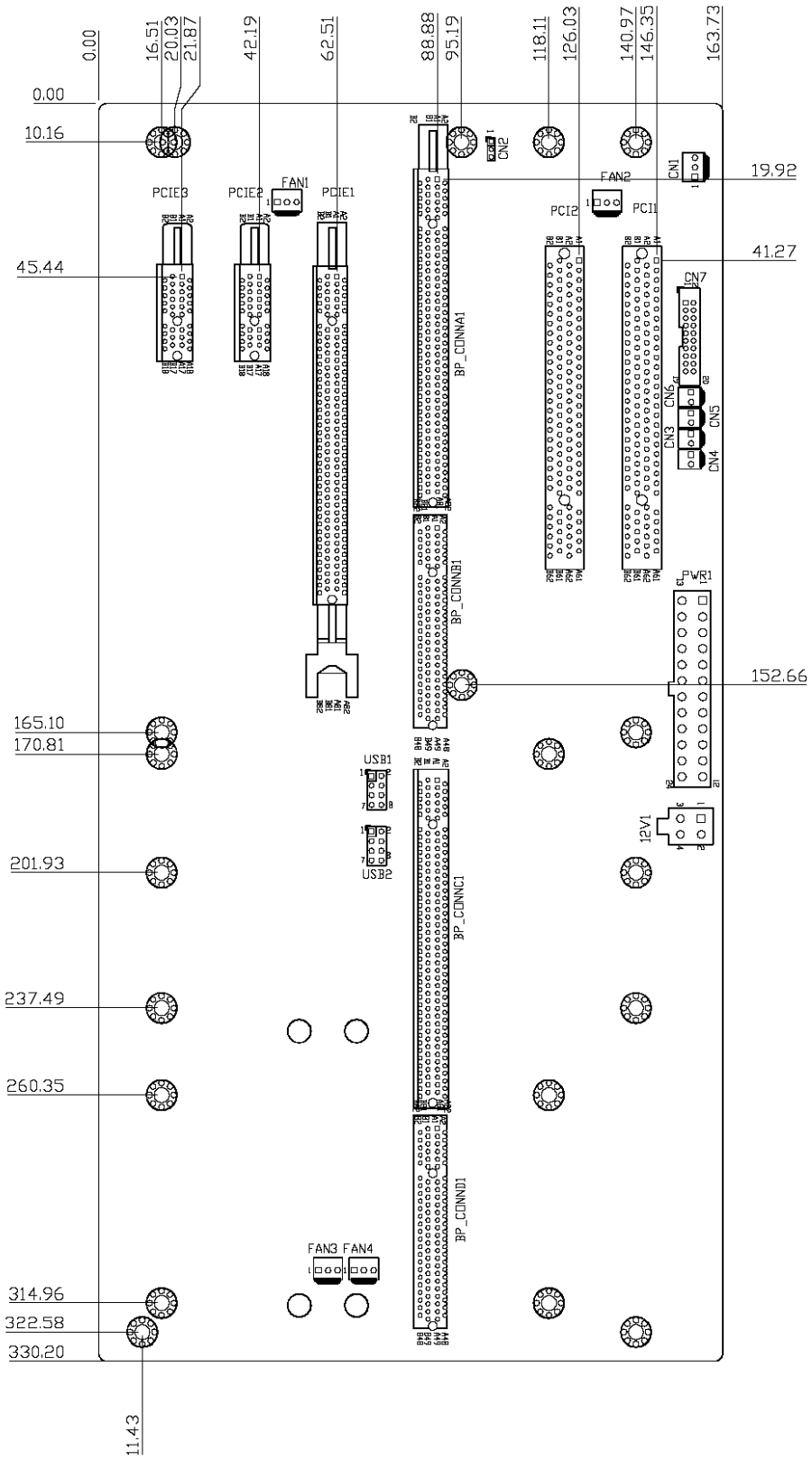


PIN	DESCRIPTION
1	SMDATA
2	SMBCLK

9 CN7: The I/O Power Connector indicators for controlling your CPU board.

PIN	DESCRIPTION	PIN	DESCRIPTION
1	GND	2	-12V
3	+12V	4	GND
5	IPMB_DA	6	5VSB
7	GND	8	GND
9	IPMB_CL	10	5VSB
11	+5V	12	PWRGD
13	SMDAT	14	SHB_RST#
15	5VSB	16	GND
17	SMCLK	18	PSON#
19	+3.3V	20	PWRBT#



10 PCI Routing table:

SIGNAL	REQ#	GNT#	IDSEL	INTA#	INTB#	INTC#	INTD#
PIN No.	B18	A17	A26	A6	B7	A7	B8
PCI1	REQ0#	GNT0#	AD31	B	C	D	A
PCI2	REQ1#	GNT1#	AD30	C	D	A	B